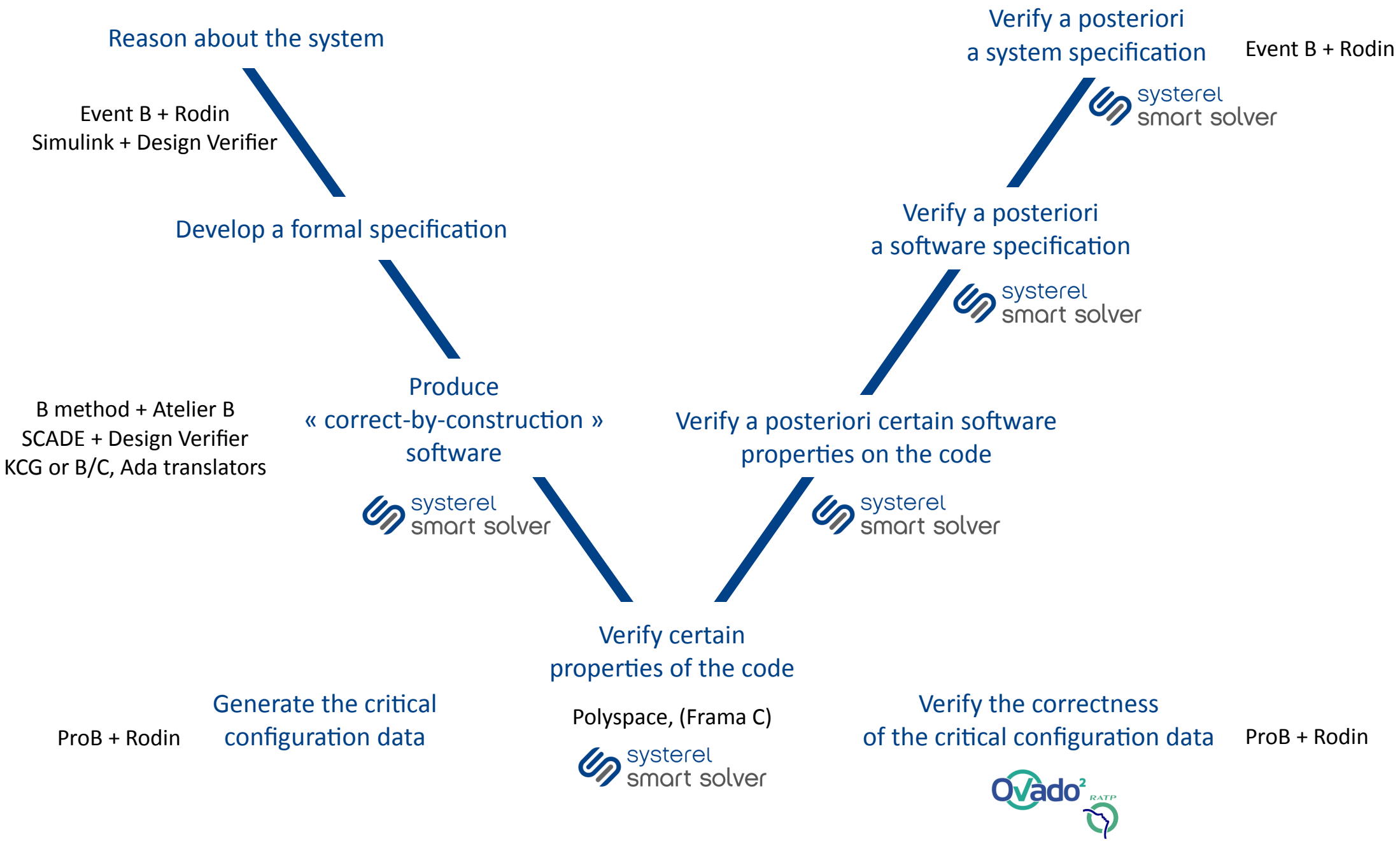
Reason about the system
Event B + Rodin
Simulink + Design Verifier
Develop a formal specification
B method + Atelier B
SCADE + Design Verifier
KCG or B/C, Ada translators
Produce
« correct-by-construction »
software
systerel
smart solver
Verify certain
properties of the code
Polyspace, (Frama C)
systerel
smart solver
Verify a posteriori certain software
properties on the code
systerel
smart solver
Verify a posteriori
a software specification
systerel
smart solver
Verify a posteriori
a system specification
Event B + Rodin
systerel
smart solver
Generate the critical
configuration data
ProB + Rodin
Verify the correctness
of the critical configuration data
ProB + Rodin
Ovado²
RATP